



Global Communication Semiconductors, LLC

Corporate & Foundry
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Test Engineering Manager

Opened: 1/23/25
Job Req: #0789
Department: Technology
Status: Full-Time/Hourly
Pay Range: \$120,000 - \$150,000
Shift: Day

Job Description

- Manage and supervise Test Engineering group of 4-6 people.
- Responsible for setting up infrastructures of DC, RF, Reliability automatic testing functions for RF technology, Optoelectronics technology and Manufacturing.
- Support trouble-shooting, failure analysis and yield improvement in various technologies such as pHEMT, HFET, HBT, GaN HEMT and Opto devices and their production processes.
- Maintain and manage automatic testing data and their data base. Assist integration engineering in necessary data analysis and trend charts generation.

Job Requirements

- PhD or MS in EE, Physics or Materials Science or equivalent.
- More than 7-10 years of industry experience in semiconductor wafer fab environment desirable.